

AA-64 architecture debug registers

debug registers DR0..15																															
reg.	6	6	6	6	5	5	5	5	5	5	5	5	5	5	4	4	4	4	4	4	4	4	4	4	3	3	3	3	3	3	3
	3	3	2	2	2	2	2	2	2	2	2	1	1	1	1	1	1	1	1	1	1	1	9	8	7	6	5	4	3	2	1
DR0	breakpoint #0 virtual address																														
	breakpoint #0 virtual address																														
DR1	breakpoint #1 virtual address																														
	breakpoint #1 virtual address																														
DR2	breakpoint #2 virtual address																														
	breakpoint #2 virtual address																														
DR3	breakpoint #3 virtual address																														
	breakpoint #3 virtual address																														
DR4	reserved																														
	reserved																														

DR5	reserved																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																													
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	reserved
DR13	reserved
	reserved
DR14	reserved
	reserved
DR15	reserved
	reserved

note: In LM a LENn value of 10b is no longer reserved. Instead it selects an 8 byte breakpoint.

