

AA-64 architecture table registers

GDTR, IDTR, LDTR, and TR																internal descriptor cache			
1	1	1	1	1	1	9	8	7	6	5	4	3	2	1	0				
GDTR																64bit base	res.	16bit limit	access rights
IDTR																64bit base	res.	16bit limit	access rights
LDTR selector																64bit base	32bit limit		access rights
TR selector																64bit base	32bit limit		access rights

