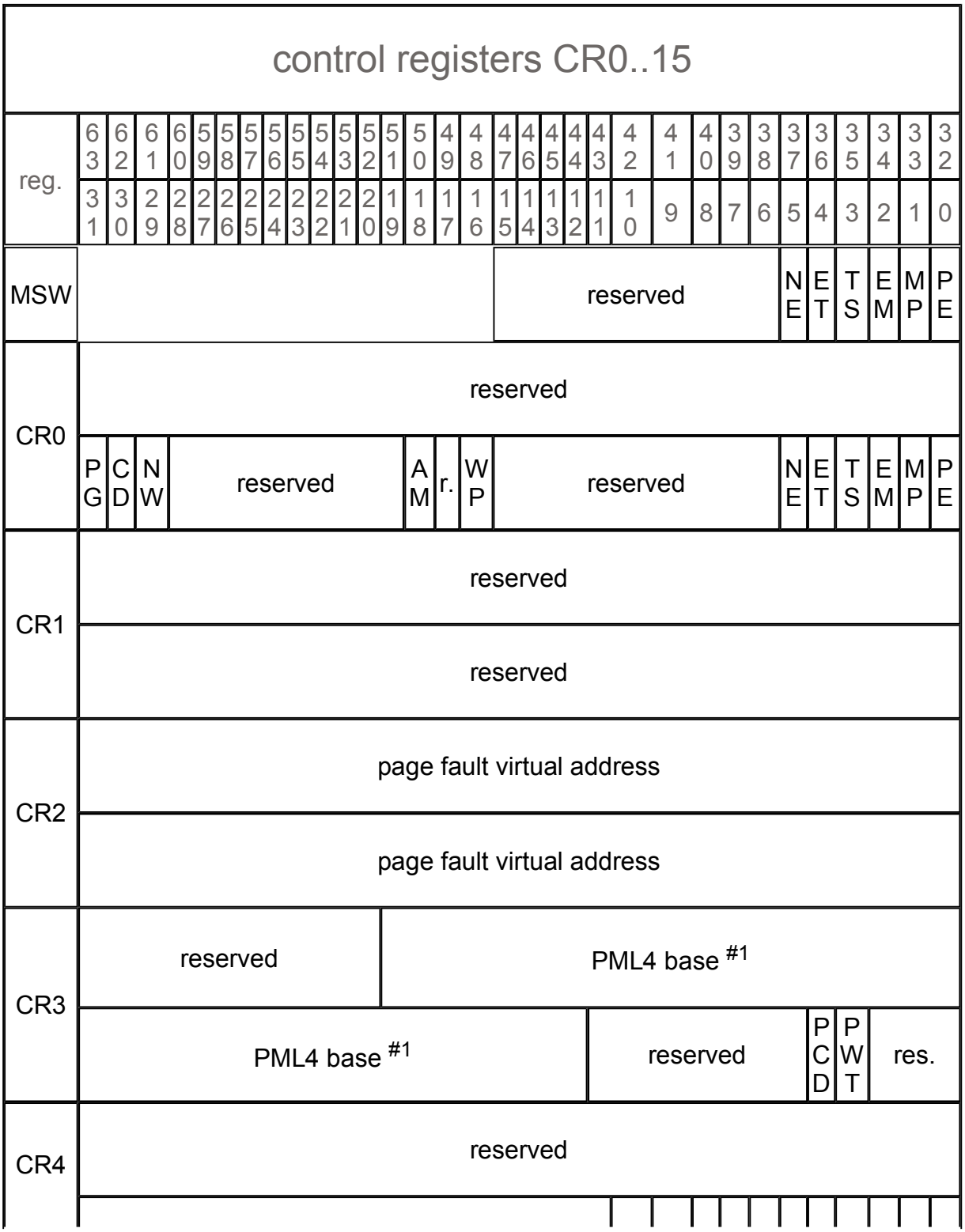
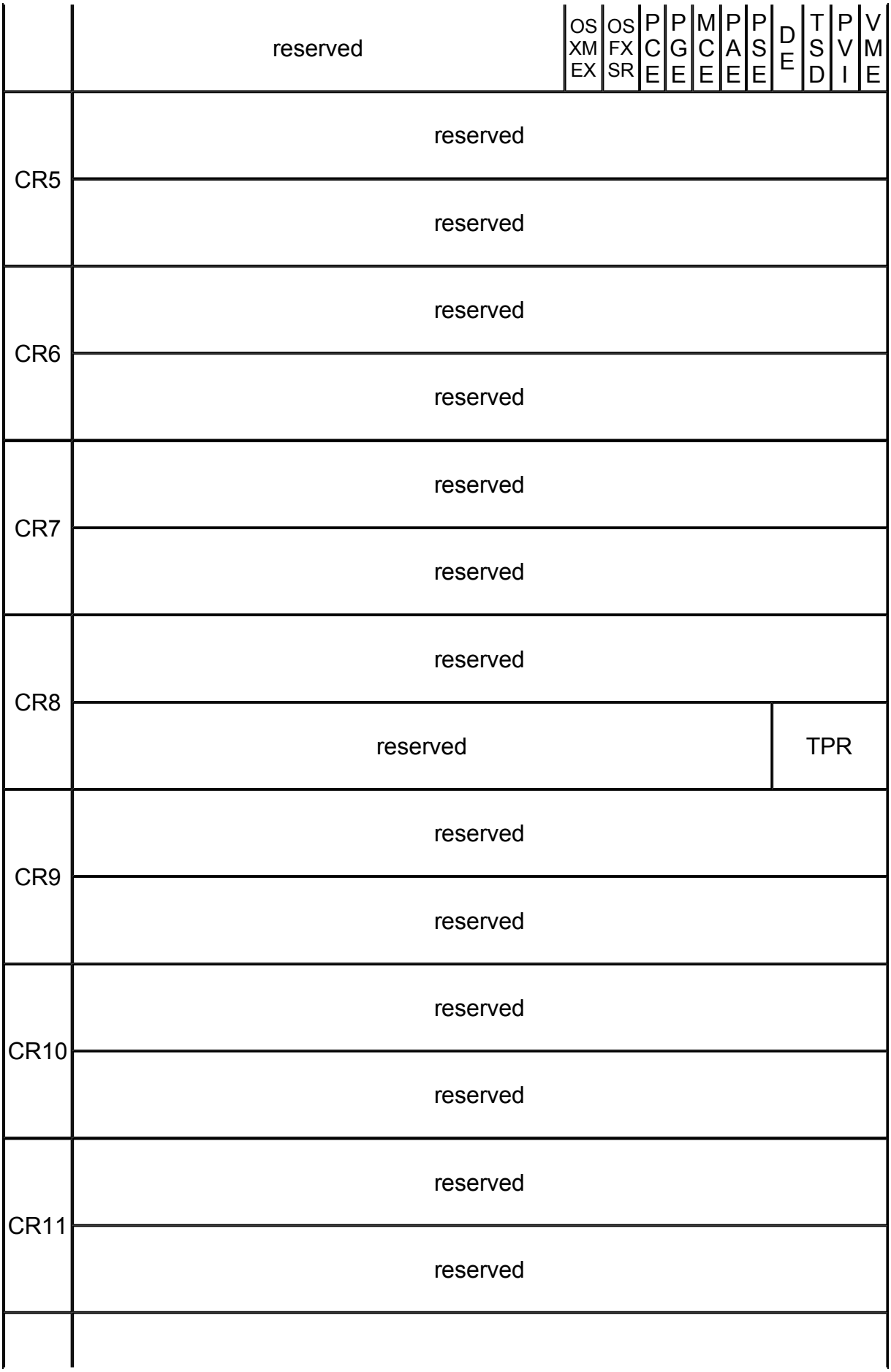
AA-64 architecture control registers




reserved
OSXMEX
OSFXSR
PCE
PGE
MCE
PAE
PSE
DE
TSD
PVI
VME
CR5
reserved
reserved
CR6
reserved
reserved
CR7
reserved
reserved
CR8
reserved
reserved
TPR
CR9
reserved
reserved
CR10
reserved
reserved
CR11
reserved
reserved

CR12	reserved
	reserved
CR13	reserved
	reserved
CR14	reserved
	reserved
CR15	reserved
	reserved

note	description
#1	The number of actually implemented bits depends on the number of physical address bits. The remaining bits are reserved if less than 52 physical address bits are implemented.